

INFORMATION DISCLOSURE STATEMENT	Applicant:	Junichiro WATANABE
	Application No.	Not yet assigned
	Filed:	January 29, 2004
	For:	Semiconductor Integrated Circuit
	Group Art Unit:	Not yet assigned
	Examiner:	Not yet assigned
	Attorney Docket No.:	2102475-991320

Commissioner of Patents and Trademarks P.O. Box 1450 Alexandria, VA 22313-1450		
Dear Sir:		
In accordance with the provisions of 37 C.F.R. § 1.56(a) and 37 C.F.R. § 1.97, Applicant(s) hereby make of record the references listed on the accompanying Form PTO-1449 for consideration by the Examiner in connection with the examination of the above-identified patent application.		
This Information Disclosure Statement:		
(a)	<input checked="" type="checkbox"/>	accompanies a new patent application submitted herewith.
(b)	<input type="checkbox"/>	is filed within three (3) months of the Filing Date or before the mailing date of a First Office Action on the merits; OR
(c)	<input type="checkbox"/>	after the period defined in (b) but before the mailing date of a Final Rejection or Notice of Allowance, OR
(d)	<input type="checkbox"/>	is filed after the first Office Action and more than three months after the application's filing date or PCT national stage date of entry filing but, as far as is known to the undersigned prior to the mailing date of either a final rejection or a notice of allowance, and is accompanied by either the fee (\$180) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below OR
(e)	<input type="checkbox"/>	is filed after the mailing date of either a final rejection or a notice of allowance, and the issue fee has not been paid, and is accompanied by the requisite petition fee (\$130) set forth in 37 CFR § 1.17(l)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.
As required under § 1.97(e), Applicants, through the undersigned, hereby state either that [check the appropriate space]:		
(f)	<input type="checkbox"/>	Each item of information contained in the Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing date of the Information Disclosure Statement; or
(g)	<input type="checkbox"/>	No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this Statement after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

It is respectfully requested that each of the references shown on the attached Form PTO-1449 be made of record in this application.

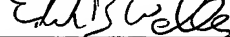
A copy of the Japanese Patent No. 2001-244338 is enclosed. A concise explanation of relevance: An ESD protection circuit of the signal input terminal uses a fuse element and a diode element. When operating, an input protection diode is cut from a signal input terminal.

The Commissioner is authorized to charge any deficiencies and credit any overpayment of fees to our Deposit Account No. 07-1896.

Respectfully submitted,

Date: January 29, 2004

GRAY CARY WARE & FREIDENRICH LLP

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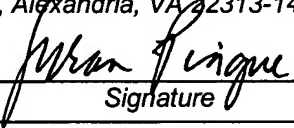
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as EXPRESS MAIL (EV 302279970) in an envelope addressed to: Commissioner of Patents & Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450.

January 29, 2004

Date


Signature

Form PTO-1449 (modified 2/91) INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	U.S. DEPT. OF COMMERCE Patent and Trademark Office		Attorney Docket Number: 2102475-991320	Serial Number: Not yet assigned
	Applicant: Watanabe			
	Filing date: January 29, 2004		Group art unit: Not yet assigned	

U.S. PATENT DOCUMENTS

Examiner Initial	Patent number	Date	Name	Class	Sub-class	Filing date if appropriate
	6,327,125	12-2001	Colclaser et al			

FOREIGN PATENT DOCUMENTS

	Document number	Filing Date	Country	Class	Sub-class	Translation YES	NO
	2001-244338	09-2001	Japan				X

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner:	Date Considered:
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP '609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	